

5. (Amended) A substrate bias generator according to claim 4, wherein said controller circuit comprises:

a NOR circuit; and

an AND circuit connected to an output of said NOR circuit.

Kindly add the following new claim.

--7. A substrate bias generator according to claim 1, wherein said controller comprises:

a NOR circuit having an input of said chip active enable signal and having input thereto an inversion of said self refresh enable signal; and

an AND circuit having input thereto said output signal of said substrate voltage level detector and an output signal of said NOR circuit, said AND circuit controlling said first PMOS transistor.--

REMARKS

Reconsideration and allowance in view of the foregoing amendments and the following remarks, as well as entry of this

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amendment pursuant to Rule 116, are respectfully requested. Upon entry of this amendment, claims 1-5 and 7 will remain pending.

Claims 1-5 and 7 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.

Although claim 1 has been cancelled, the rejection of claim 1 related to the "channel" is also present in other claims and will, therefore, be addressed. With respect to this rejection, the Examiner's comment that it is "notoriously well known that enhancement mode transistors do not have a channel" is simply not understood. When turned on, a channel is created in the enhancement mode transistors that allows current to flow. In fact, U.S. Patent No. 5,329,168 to Sugibayashi et al., which the Examiner relies upon in making his art rejection, similarly uses the term "channel" when describing enhancement mode transistors. Accordingly, k withdrawal of this rejection is respectfully requested.

Claims 1 and 4-5 stand rejected under 35 U.S.C. § 103 as being unpatentable over U.S. Patent No. 5,329,168 to Sugibayashi et al. ("Sugibayashi").

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All objections and rejections having been addressed, it is respectfully submitted that the present application is in condition for allowance and a Notice to that effect is earnestly solicited.

Respectfully submitted,

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